



PRODUCT SNAPSHOT

PCI Express® Compliant Switches & Bridges

PLX is the World's Largest Provider of PCIe® Interconnect Switches and Bridges

ExpressLane™ PCIe Switches															PCI EXPRESS	
PCI-SIG® Base Spec.	Part Number	Lanes	Ports	Latency (ns)	Multi-Root/ Multi-Host	Multi-cast or Dual Cast™	Read Pacing™	ACS/ ARI*	DBA*	NT*	DMA*	HPC*	VCs*	SSC*	Power Typ. (W)	Package (mm²)
r2.0	PEX 8696	96	24	176	Yes	MC	Yes	Yes	Yes	Yes	—	4	1	—	10.2	35x35
r2.0	PEX 8680	80	20	176	Yes	MC	Yes	Yes	Yes	Yes	—	4	1	—	9.0	35x35
r2.0	PEX 8664	64	16	176	Yes	MC	Yes	Yes	Yes	Yes	—	4	1	—	7.9	35x35
r2.0	PEX 8649	48	12	176	Yes	MC	Yes	Yes	Yes	Yes	—	3	1	—	6.7	27x27
r2.0	PEX 8648	48	12	140	—	DC	Yes	Yes	Yes	Yes	—	3	1	—	3.7	27x27
r2.0	PEX 8647	48	3	140	—	DC	Yes	Yes	Yes	—	—	0	1	—	2.8	27x27
r2.0	PEX 8636	36	24	200	Yes	MC	Yes	Yes	Yes	Yes	—	4	1	—	8.8	35x35
r2.0	PEX 8632	32	12	145	—	DC	Yes	Yes	Yes	Yes	—	3	1	—	2.6	27x27
r2.0	PEX 8625	24	24	200	Yes	MC	Yes	Yes	Yes	Yes	—	4	1	—	8.5	35x35
r2.0	PEX 8624	24	6	145	—	DC	Yes	Yes	Yes	Yes	—	3	1	—	1.8	19x19
r2.0	PEX 8619	16	16	140	—	DC	Yes	Yes	Yes	Yes	Yes	^^	2	Yes	2.0	19x19
r2.0	PEX 8618	16	16	140	—	DC	Yes	Yes	Yes	Yes	—	^^	2	Yes	1.9	19x19
r2.0	PEX 8617	16	4	140	—	DC	Yes	Yes	Yes	Yes	—	^^	2	Yes	1.9	19x19
r2.0	PEX 8616	16	4	150	—	DC	Yes	Yes	Yes	Yes	—	2	1	—	1.6	19x19
r2.0	PEX 8615	12	12	140	—	DC	Yes	Yes	Yes	Yes	Yes	^^	2	Yes	1.8	19x19
r2.0	PEX 8614	12	12	140	—	DC	Yes	Yes	Yes	Yes	—	^^	2	Yes	1.7	19x19
r2.0	PEX 8613	12	3	140	—	DC	Yes	Yes	Yes	Yes	—	^^	2	Yes	1.7	19x19
r2.0	PEX 8612	12	3	150	—	DC	Yes	Yes	Yes	Yes	—	2	1	—	1.5	19x19
r2.0	PEX 8609	8	8	140	—	DC	Yes	Yes	Yes	Yes	Yes	^^	2	Yes	1.6	15x15
r2.0	PEX 8608	8	8	140	—	DC	Yes	Yes	Yes	Yes	—	^^	2	Yes	1.4	15x15
r2.0	PEX 8606	6	6	190	—	DC	Yes	Yes	Yes	Yes	—	^^	2	Yes	1.3	15x15
r2.0	PEX 8604	4	4	190	—	DC	Yes	Yes	Yes	Yes	—	^^	2	Yes	1.3	15x15
r1.1 / 1.0a	PEX 8548	48	9	110	—	—	—	—	Yes	—	—	3	1	—	4.9	37.5x37.5
r1.1 / 1.0a	PEX 8547	48	3	110	—	—	—	—	Yes	—	—	0	1	—	4.9	37.5x37.5
r1.1 / 1.0a	PEX 8533	32	6	115	—	—	—	—	Yes	—	—	3	1	—	3.3	35x35
r1.1 / 1.0a	PEX 8532	32	8	275	—	—	—	—	—	Yes	—	8	2	—	5.7	35x35
r1.1 / 1.0a	PEX 8525	24	5	115	—	—	—	—	Yes	—	—	3	1	—	2.6	31x31
r1.1 / 1.0a	PEX 8524	24	6	275	—	—	—	—	—	Yes	—	6	2	—	3.9	31x31
r1.1 / 1.0a	PEX 8518	16	5	150	—	—	—	—	—	Yes	—	5	2	Yes	2.6	23x23
r1.1 / 1.0a	PEX 8517	16	5	150	—	—	—	—	—	Yes	—	4	2	—	2.6	27x27
r1.1 / 1.0a	PEX 8516	16	4	275	—	—	—	—	—	Yes	—	4	2	—	2.6	27x27
r1.1 / 1.0a	PEX 8512	12	5	150	—	—	—	—	—	Yes	—	5	2	Yes	2.2	23x23
r1.1 / 1.0a	PEX 8509	8	8	118	—	—	—	—	Yes	—	—	3	1	—	1.2	15x15
r1.1 / 1.0a	PEX 8508	8	5	150	—	—	—	—	—	Yes	—	5	2	Yes	1.6	19x19
r1.1 / 1.0a	PEX 8505	5	5	138	—	—	—	—	Yes	—	—	3	1	—	0.8	15x15

*ACS = Access Control Service; ARI = Alternative Routing-ID Interpretation; DBA = Dynamic Buffer Allocation; NT = Non-Transparency; DMA = Direct Memory Access; HPC = Hot-Plug Controllers; VCs = Virtual Channels; SSC = Spread Spectrum Clock Isolation; ^^ = Hot-Plug control via I²C; Gen 2 devices are fully backwards compatible to r1.1/1.0a and recommended for all new designs

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PCI Express® Compliant Bridges

ExpressLane™ PCIe to PCI and PCI-X Bridges									PCI EXPRESS
PCI-SIG® Base Spec.	Part Number	Lanes	Bus Interface	Reverse Mode	Forward Mode	Power	PCI Masters	GPIO	Package Size (mm²)
r1.0a	PEX 8112	1	PCIe to PCI	Yes	Yes	400mW	4	4	10x10 13x13
r1.0a	PEX 8114	4	PCIe to PCI-X	Yes	Yes	2W	4	0	17x17

ExpressLane™ PCIe to Local Bus Bridge								PCI EXPRESS
PCI-SIG® Base Spec.	Part Number	Lanes	Description	DMA Channels	Integrated SerDes	Power	Root Complex Mode	Package Size (mm²)
r1.0a	PEX 8311	1	8/16/32-bit, 66 MHz	2	Yes	1W	Yes	21x21

PCIe Serial Bridges - UARTs													PCI EXPRESS	
PCI-SIG® Base Spec.	Part Number	UARTs	RS232/RS422/RS485	Max Baud (Mbps)	Data Bits (#)	FIFO Depth (byte)	FIFO Trig Levels	Clock Pre-scaler	Auto XON/XOFF	Auto RTS/CTS	IrDA	Parallel	GPIO	Temp Range
r1.1	OXPCle952	2	Yes	15	5-9	128	Yes	Yes	Yes	Yes	Yes	PP	8	Ind.
r1.1	OXPCle954	4	Yes	15	5-9	128	Yes	Yes	Yes	Yes	Yes	--	8	Ind.
r1.1	OXPCle958	8	Yes	15	5-9	128	Yes	Yes	Yes	Yes	Yes	--	8	Ind.
r1.1	OXPCle840						--					PP	8	Ind.

PCIe to USB and UART Bridge															PCI EXPRESS
PCI-SIG® Base Spec.	Part Number	Dual USB 2.0 Host	USB 2.0 & SPI/SRAM	HS USB 2.0 ULPI Port	FS USB 2.0 Ply	OHCI/EHCI Compliant	USB Suspend/Resume	Internal Memory (kB)	FIFO & Direct Modes	Master & Slave Modes	Max SPI (Mb/s)	Max SRAM (MB/s)	950 UART	GPIO	Temp Range
r1.1	OXPCle200	Yes	Yes	Host	Host	Yes	Yes	8	Yes	Yes	60	25	Yes	8	Ind.

Other PLX Products:

- Storage SoC Controllers for NAS & DAS
- PCI to PCI, & PCI to PCI-X Bridges
- PCI to Local Bus & Target Bridges
- Serial Bridges - UARTs
- USB 2.0 High Speed Controllers

